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843.37558VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M. FUNABASHI

Serial No.: Not Yet Assigned

(Divisional Application of SN 09/392,568

Filed: September 9, 1999)

Filed:

July 12, 2001

For:

METHOD OF MANUFACTURING A SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

July 12, 2001

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Please amend the above-identified application, prior to examination thereof, as follows:

IN THE SPECIFICATION:

Please add the following new paragraph on page 1, between the title and the heading "BACKGROUND OF THE INVENTION":

-- This application is a Divisional application of prior application Serial No. 09/392,568, filed September 9, 1999. --.

Please delete the paragraph bridging pages 2 and 3, and substitute therefor the following new paragraph:

A2

-- Japanese Patent Application Laid-Open No. 6-41773 discloses a technique in which an anti-adsorption agent (such as NTPO, EDTPO, EDDPO, or the like) is added to an aqueous solution containing an alkaline component such as ammonium hydroxide, hydroxide of alkaline metal (KOH or NaOH), choline,